

## Electronic Patent Application Fee Transmittal

Application Number:	10828884
Filing Date:	21-Apr-2004
Title of Invention:	COMPENSATED LINEARITY VOLTAGE-CONTROL-CAPACITOR DEVICE BY STANDARD CMOS PROCESS
First Named Inventor/Applicant Name:	Bor-Min Tseng
Filer:	Roger Clark Knapp/Kristin Hayes
Attorney Docket Number:	TSM03-0763

Filed as Large Entity

### Utility Filing Fees

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
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Basic Filing:

Pages:

Claims:

Miscellaneous-Filing:

Petition:

Patent-Appeals-and-Interference:

Post-Allowance-and-Post-Issuance:

Utility Appl Issue fee	1501	1	1400	1400
Publ. Fee- early, voluntary, or normal	1504	1	300	300

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Extension-of-Time:</b>				
<b>Miscellaneous:</b>				
<b>Total in USD (\$)</b>				<b>1700</b>